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# CDF Operations Report

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All Experimenters' Meeting



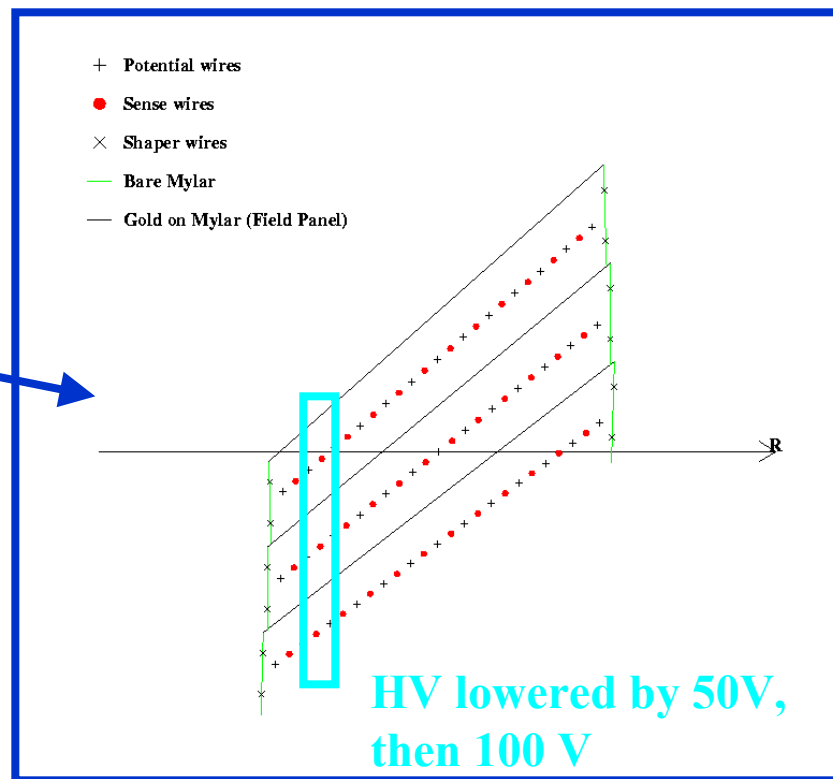
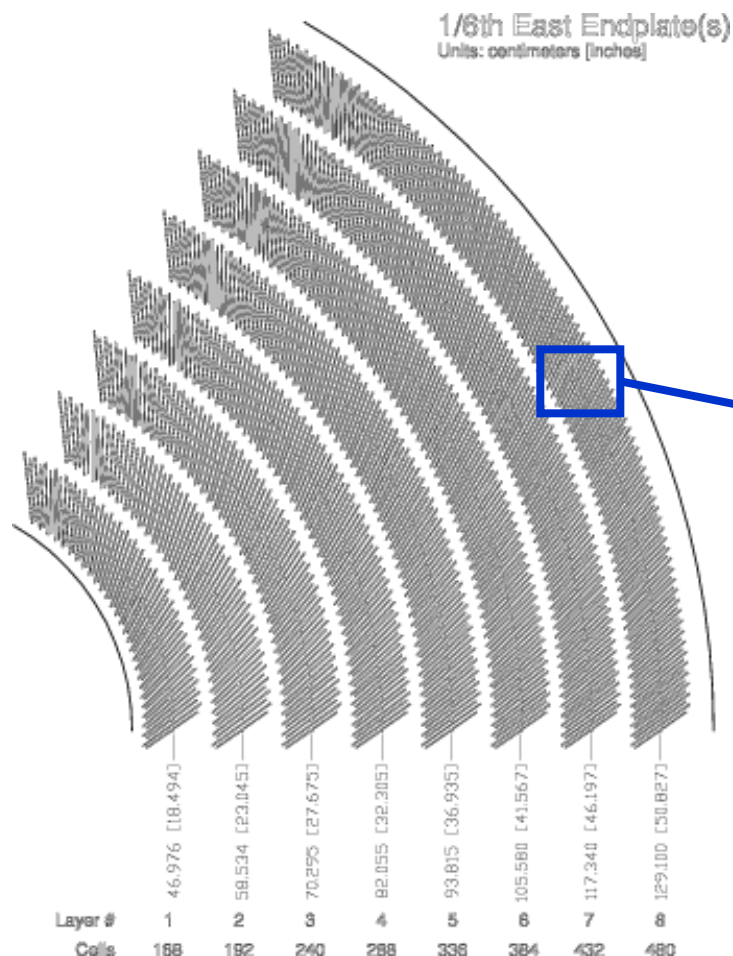
# Stores summary

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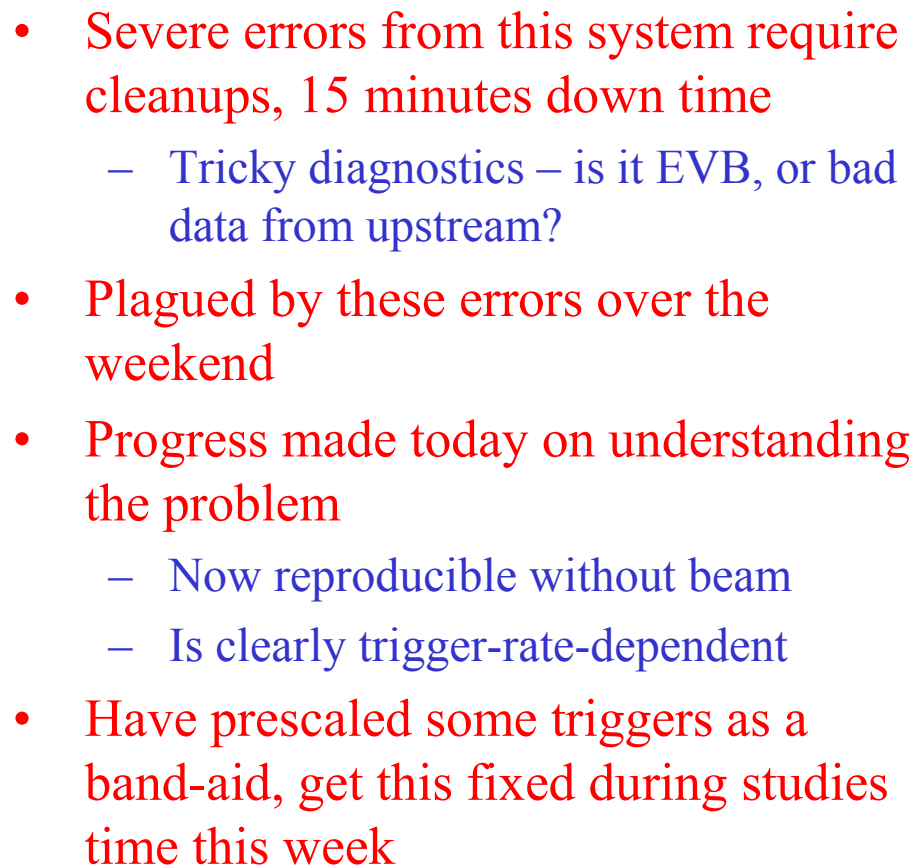
Date	Store	Inst Lum (initial)	Int Lum (delivered)	Int Lum (live)	$\epsilon$	Comments
Tu 4/15	2433	35.0e30	933.9	827.2	88.6%	COT trip
Th 4/17	2436	33.5e30	1184.4	960.9	81.1%	COT trip, stuck SVX, VRB failure
Th 4/17	2438	33.1e30	1260.9	1163.0	92.2%	COT trip
Fr 4/18	2441	35.7e30	1401.0	1019.0	72.7%	COT/CPR/L00 trips, SVX fuse
Sa 4/19	2443	22.0e30	984.9	775.2	78.7%	COT trip, stuck SVX, EVB problems
Su 4/20	2445	35.8e30	1374.5	930.7	67.7%	EVB problems
Total			7.1 pb <sup>-1</sup>	5.7 pb <sup>-1</sup>	79.5%	4.9 pb <sup>-1</sup> with silicon



# COT SL8 trips



- Triggering still OK – offline data....?
- 1 trip/store last week, twice in current store
- Need 2-shift access to disable wire





# Silicon FTMs

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- Missing certain clock signals can result in the SVX3D chips getting “stuck”
  - Leads to high current states, which can lead to
    - Trips (= down time)
    - Broken wire bonds (worst case scenario)
- Re-initting chips (few seconds down time) gets them unstuck, but
  - Persistent reoccurrences trigger safety concerns – wedge must be removed from data-taking (= down time)
- Problem fixed with 100% success rate by “buffing” FTMs
  - Request access (~1 hr) when wedges are removed, go in and buff them
  - Make use of all other accesses to buff the whole system (33/58 done)
- High confidence in this fix now; would like to get it all over with in one long access



# Conclusions

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- Wish CDF's weekend was as good as the accelerator's
- We are formulating a plan on how to deal with the need for a long COT/silicon access (-es?)
- Get EVB straightened out during studies this week
- New team of shift crews has taken over – expect them to get more efficient as they learn the tricks of the trade